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### Search Results -

Term	Documents
(13 AND 27).PGPB,USPT,USOC,EPAB,JPAB,DWPI,TDBD.	8
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## Search History

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Set Name side by side	Query		Set Name result set
DB=B	PGPB, USPT, USOC, EPAB, JPAB, DWPI, TDBD; PLUR=YES; OP=OR		
<u>L32</u>	L27 and 113	8	<u>L32</u>
<u>L31</u>	L27 and 112	0	<u>L31</u>
<u>L30</u>	L27 and 19	70	<u>L30</u>
<u>L29</u>	L27 and 18	94	<u>L29</u>
<u>L28</u>	L27 and 16	153	<u>L28</u>
<u>L27</u>	L26 and retir\$7	178	<u>L27</u>
<u>L26</u>	15 and (decod\$3 or predecod\$4)	210	<u>L26</u>
DB=I	PGPB,USPT; PLUR=YES; OP=OR	•	
<u>L25</u>	14 and 113	10	<u>L25</u>
<u>L24</u>	14 and 112	0	<u>L24</u>
<u>L23</u>	14 and 110	55	<u>L23</u> .

<u>L22</u>	14 and 19	78	<u>L22</u>
<u>L21</u>	14 and 18	116	<u>L21</u>
<u>L20</u>	14 and 16	178	<u>L20</u>
<u>L19</u>	13 and 113	17	<u>L19</u>
<u>L18</u>	13 and 112	Ó	L18
<u>L17</u>	13 and 110	63	<u>L17</u>
<u>L16</u>	13 and 19	109	<u>L16</u>
<u>L15</u>	13 and 18	150	<u>L15</u>
<u>L14</u>	13 and 16	236	<u>L14</u>
<u>L13</u>	(718/102-108)[CCLS]	4445	<u>L13</u>
<u>L12</u>	(717/159-162)[CCLS]	955	<u>L12</u>
<u>L11</u>	(711/118-221)[CCLS]	25719	<u>L11</u>
<u>L10</u>	(711/118-221)![CCLS]	25719	<u>L10</u>
<u>L9</u>	(712/230-248)![CCLS]	3335	<u>L9</u>
<u>L8</u>	(712/205-219,225-228)[CCLS]	5731	<u>L8</u>
<u>L7</u>	(712/2-300)[CCLS]	12968	<u>L7</u>
<u>L6</u>	(712/2-300)![CCLS]	12968	<u>L6</u>
DB=B	PGPB,USPT,USOC,EPAB,JPAB,DWPI,TDBD; PLUR=YES; OP=OR		
<u>L5</u>	branch\$3 and 14	210	<u>L5</u>
<u>L4</u>	L3 and (reorder\$4 or order\$5 or rearrang\$7 out near3 (sequenc\$4 or order)) near25 (buffer\$4 or fifo or filo or lifo)	210	<u>L4</u>
<u>L3</u>	L2 and (predict\$5 or speculat\$5 ) near5 taken	281	<u>L3</u>
<u>L2</u>	L1 and renam\$4	610	<u>L2</u>
<u>L1</u>	(prefetch\$6 or fetch\$7) near15 (concurrent\$4 or simultaneous\$4 or parallel\$5)	7279	<u>L1</u>

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IEEE STD IEEE Standard	, .		Huiyang Zhou; <u>Computer Architecture Letters, IEEE</u> Volume 5, Issue 1, JanJune 2006 Page(s):22 - 25  Digital Object Identifier 10.1109/L-CA.2006.1
			AbstractPlus   Full Text: PDF(184 KB) IEEE JNL Rights and Permissions
	Γ	<b>2</b> .	A seventh-generation x86 microprocessor  Golden, M.; Hesley, S.; Scherer, A.; Crowley, M.; Johnson, S.C.; Meier, S.; Meyer, D.; Moench, Partovi, H.; Weber, F.; White, S.; Wood, T.; Yong, J.; Solid-State Circuits. IEEE Journal of  Volume 34, Issue 11, Nov. 1999 Page(s):1466 - 1477  Digital Object Identifier 10.1109/4.799851
			AbstractPlus   References   Full Text: PDF(468 KB)   IEEE JNL   Rights and Permissions
	Γ	<b>3.</b>	Speculative data-driven multithreading Roth, A.; Sohi, G.S.; High-Performance Computer Architecture, 2001. HPCA, The Seventh International Symposium 19-24 Jan. 2001 Page(s):37 - 48 Digital Object Identifier 10.1109/HPCA.2001.903250
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	<b>Γ</b> .	<b></b>	A 7 <sup>th</sup> -generation x86 microprocessor Hesley, S.; Andrade, V.; Burd, B.; Constant, G.; Correll, J.; Crowley, M.; Golden, M.; Hopkins, N Khondker, R.; Meyer, D.; Moench, J.; Partovi, H.; Posey, R.; Weber, F.; Yong, J.; Solid-State Circuits Conference, 1999. Digest of Technical Papers. ISSCC, 1999 IEEE International Papers. ISSCC,
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		5.	A Case for Fault Tolerance and Performance Enhancement Using Chip Multi-Processors Zhou Huiyang;

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